

# Review of Transformer less PV system in a multilevel grid connected inverter

A.M.Mendhe<sup>1</sup>, R.P.Singh<sup>2</sup>

<sup>1</sup>Reaserch Scholar, SSSUTMS, Sehore (MP), (India)

<sup>2</sup>Vice Chancellor., SSSUTMS, Sehore (MP), (India)

## ABSTRACT

*This paper presents work done in a Transformer less PV system in the multi grid connected inverter in which a single-phase transformer less grid-connected photovoltaic converter based on two cascaded full bridges with different dc-link voltages. The converter can synthesize up to different voltage levels with a single dc bus, since one of the full bridges is supplied by a flying capacitor. The multilevel output reduces harmonic distortion and electromagnetic interference. A suitable switching strategy is employed to regulate the flying-capacitor voltage, improve the efficiency (most devices switch at the grid frequency), and minimize the common-mode leakage current with the help of a novel dedicated circuit (transient circuit). Simulations and experiments confirm the feasibility and good performance of the proposed converter.*

**Keyword:** *Leakage current, multilevel system, PV system, flying capacitor.*

## 1.INTRODUCTION

Grid connected photovoltaic (PV) converters represent the most widespread solution for residential renewable energy generation. While classical designs of PV converters feature a grid frequency transformer, which is a typically heavy and costly component, at the interface between the converter and the electrical grid, researchers are now considering transformer less architectures in order to reduce costs and weight and improve efficiency. Removing the grid frequency transformer entails all the benefits above but worsens the output power quality, allowing the injection of dc current into the grid [1], [2] and giving rise to the problem of ground leakage current [3], [4]. Although the active parts of PV modules might be electrically insulated from the ground-connected mounting frame, a path for ac ground leakage currents generally exists due to a parasitic capacitance between the modules and the frame and to the connection between the neutral wire and the ground, usually realized at the low-voltage/medium-voltage (LV/MV) transformer [3]. In addition to deteriorating power quality, the ground leakage current increases the generation of electromagnetic interference and can represent a safety hazard, so that international regulations pose strict limits to its magnitude. This issue must be confronted in all transformers less PV converters, regardless of architecture. In particular, in full-bridge-based topologies, the ground leakage current is mainly due to high frequency variations of the common-mode voltage at the output of the power converter [4]. Several solutions can be found in literature aiming at the reduction of the common-mode voltage harmonic content [5]–[7]. Once the grid frequency transformer is removed from a PV converter, the bulkiest

wound and reactive components that remain are those that form the output filter used to clean the output voltage and current from high frequency switching components. Further reduction in cost and weight and improvement in efficiency can be achieved by reducing the filter size, and this is the goal of multilevel converters. Multilevel converters have been investigated for years [8], but only recently have the results of such researches found their way to commercial PV converters. Since they can synthesize the output voltages using more levels, multilevel converters outperform conventional two- and three-level converters in terms of harmonic distortion. Moreover, multilevel converters subdivide the input voltage among several power devices, allowing for the use of more efficient devices. Multilevel converters were initially employed in high-voltage industrial and power train applications. They were first introduced in renewable energy converters inside utility-scale plants, in which they are still largely employed [9]–[13]. Recently, they have found their way to residential-scale single-phase PV converters, where they currently represent a hot research topic [14]–[29]. CFB converters have also been proposed for stand-alone applications [17], [22]. CFBs give developers many degrees of freedom for the control strategy. Together with the aforementioned sequential permutation and with phase shifting [19], artificial neural networks [23] and predictive control [24] have been proposed to minimize harmonic distortion and achieve maximum power point tracking (MPPT). A CFB made up of  $n$  full bridges (and at least  $4n$  power switches) can synthesize  $2n + 1$  voltage levels when the supply voltage is the same for each full bridge. Custom architectures can generally provide more output levels with a given number.

## II. RELATED WORK

**G. Buticchi, L. Consolini, and E. Lorenzani [1]**- a low-cost nonlinear sensor for an accurate detection, free from offset problems, of the dc voltage component present in the grid voltage has been proposed. The detection of the dc voltage component was used to realize an active filter of the dc current component. The proposed solution is outlined, and then the stability issue is addressed by means of a simplified model. Experimental results confirmed that the simplified model closely approximates the real system.

**G. Buticchi and E. Lorenzani [2]**- a method to detect the dc current component flowing into the distribution power transformer with a precise measurement of the dc voltage component at the transformer winding has been presented. A magnetic sensor has been developed and implemented in a closed-loop control system to achieve a high sensitivity and guarantees a good linearity with a high rejection ratio to grid voltage variations. Simulation and experimental results confirm the effectiveness of the proposed approach.

**H. Xiao and S. Xie [3]**- the ground current in a 1.5-kW PV installation is measured under different conditions and used to build a simulation model. The installation includes a string of 16 PV panel, a full-bridge inverter, and an LCL filter. This model allows the study of the influence of the harmonics injected by the inverter on the ground current

**I T. Kerekes, R. Teodorescu, P. Rodridguez, G. Vazquez, and E. Aldabas, [7]**- a converter topology able to minimize the ground leakage current also in the case of unipolar pulse width modulation without increasing

inductive common mode filter size and preserving efficiency has been presented. Simulations and experimental results show the feasibility of the proposed solution.

**C. Townsend, T. Summers, and R. Betz [10]**- a control and modulation structure based on Model Predictive Control (MPC) is described. The scheme inherently controls the DC link voltages while also providing the ability to modify any of those voltages to meet MPPT requirements. This avoids the cost and added complexity of extra DC/DC converters that are typically required to keep the DC link voltages uniform. Simulation and experimental results are presented that confirm the correct operation of the proposed approach.

**G. Buticchi, E. Lorenzani, and G. Franceschini [14]**- a converter architecture based on a full-bridge topology with two additional power switches and two diodes connected to the midpoint of the dc link has been proposed. Since the two added levels are obtained by the discharge of the two capacitors of the dc link, the balancing of the midpoint voltage is obtained with a specific pulse width modulation (PWM) strategy. Simulation and experimental results show the effectiveness of the proposed solution.

**I. Abdalla, J. Corda, and L. Zhang [18 ]** the novelty of the proposed system is the use of a fully FLC (not requiring any optimal PWM switching-angle generator and proportional-integral controller) and the use of an H-bridge power-sharing algorithm. Most of the required signal processing is performed by a mixed-mode field-programmable gate array, resulting in a fully integrated System-on-Chip controller. The general architecture of the system and its main performance in a large spectrum of practical situations are presented and discussed. The proposed system offers improved performance over two-level inverters, particularly at low-medium power

**J. Chavarria, D. Biel, F. Guinjoan, C. Meza, and J. Negroni [19]** an energy-balance control strategy for a cascaded single-phase grid-connected H-bridge multilevel inverter linking  $n$  independent photovoltaic (PV) arrays to the grid is presented. The control scheme is based on an energy-sampled data model of the PV system and enables the design of a voltage loop linear discrete controller for each array, ensuring the stability of the system for the whole range of PV array operating conditions. The control design is adapted to phase-shifted and level-shifted carrier pulse width modulations to share the control action among the cascade-connected bridges in order to concurrently synthesize a multilevel waveform and to keep each of the PV arrays at its maximum power operating point. Experimental results carried out on a seven-level inverter are included to validate the proposed approach.

**G. Grandi, C. Rossi, D. Ostojic, and D. Casadei [21]** a novel scheme for three-phase grid-connected photovoltaic (PV) generation systems is presented in this paper. The scheme is based on two insulated strings of PV panels, each one feeding the dc bus of a standard two-level three-phase voltage-source inverter (VSI). The inverters are connected to the grid by a three-phase transformer having open-end windings on the inverter side. The resulting conversion structure performs as a multilevel power active filter (equivalent to a three-level inverter), doubling the power capability of a single VSI with given voltage and current ratings. The multilevel voltage waveforms are generated by an improved space-vector-modulation algorithm, suitable for the implementation in industrial digital signal processors. An original control method has been introduced to regulate the dc-link voltages of each VSI, according to the voltage reference given by a single maximum power

point tracking controller. The proposed regulation system has been verified by numerical simulations and experimental tests with reference to different operating conditions

**S. Daher, J. Schmid, and F. Antunes [22]** a new high-efficiency topology for transformer less systems has been proposed , which does not generate common-mode currents and topologically guarantees that no dc is injected into the grid. The proposed topology has been verified in a 5-kW prototype with satisfactory results.

### III.CONCLUSION

This paper has proposed a multilevel grid-connected transformer less PV converter based on a CFB topology with two full bridges, one of which is supplied by a floating capacitor.

A suitable PWM strategy was developed in order to improve efficiency (most power devices commute at low frequency) and, with the help of a specific TC, minimize the ground leakage current.

The proposed PWM strategy can regulate the voltage across the flying capacitor. Simulations were performed to assess the ability to regulate the flying-capacitor voltage in a wide range of operating conditions.

Extensive simulations and experiments confirm the results of the theoretical analysis and show the good performance of the converter as far as ground leakage current and harmonic distortion are concerned. Despite the use of traditional power devices for the laboratory prototype, the experimentally measured efficiency was fairly good. The proposed converter can continuously operate at arbitrary power factors, has limited boosting capability

### REFERENCES

- [1] G. Buticchi, L. Consolini, and E. Lorenzani, "Active filter for the removal of the dc current component for single-phase power lines," *IEEE Trans. Ind. Electron.*, vol. 60, no. 10, pp. 4403–4414, Oct. 2013.
- [2] G. Buticchi and E. Lorenzani, "Detection method of the dc bias in distribution power transformers," *IEEE Trans. Ind. Electron.*, vol. 60, no. 8, pp. 3539–3549, Aug. 2013.
- [3] H. Xiao and S. Xie, "Leakage current analytical model and application in single-phase transformerless photovoltaic grid-connected inverter," *IEEE Trans. Electromagn. Compat.*, vol. 52, no. 4, pp. 902–913, Nov. 2010.
- [4] O. Lopez, F. Freijedo, A. Yepes, P. Fernandez-Comesaa, J. Malvar, R. Teodorescu, and J. Doval-Gandoy, "Eliminating ground current in a transformerless photovoltaic application," *IEEE Trans. Energy Convers.*, vol. 25, no. 1, pp. 140–147, Mar. 2010.
- [5] S. Araujo, P. Zacharias, and R. Mallwitz, "Highly efficient single-phase transformerless inverters for grid-connected photovoltaic systems," *IEEE Trans. Ind. Electron.*, vol. 57, no. 9, pp. 3118–3128, Sep. 2010.
- [6] D. Barater, G. Buticchi, A. Crinto, G. Franceschini, and E. Lorenzani, "Unipolar PWM strategy for transformerless PV grid-connected converters," *IEEE Trans. Energy Convers.*, vol. 27, no. 4, pp. 835–843, Dec. 2012.

- [7] T. Kerekes, R. Teodorescu, P. Rodridguez, G. Vazquez, and E. Aldabas, "A new high-efficiency single-phase transformerless PV inverter topology," *IEEE Trans. Ind. Electron.*, vol. 58, no. 1, pp. 184–191, Jan. 2011.
- [8] S. Kouro, M. Malinowski, K. Gopakumar, J. Pou, L. Franquelo, B. Wu, J. Rodriguez, M. P. Andrez, and J. Leon, "Recent advances and industrial applications of multilevel converters," *IEEE Trans. Ind. Electron.*, vol. 57, no. 8, pp. 2553–2580, Aug. 2010.
- [9] Y. Xue, B. Ge, and F. Z. Peng, "Reliability, efficiency, and cost comparisons of mw-scale photovoltaic inverters," in *Proc. IEEE ECCE*, Raleigh, NC, USA, Sep. 2012, pp. 1627–1634.
- [10] C. Townsend, T. Summers, and R. Betz, "Control and modulation scheme for a cascaded H-bridge multi-level converter in large scale photovoltaic systems," in *Proc. IEEE ECCE*, Raleigh, NC, USA, Sep. 2012, pp. 3707–3714.
- [11] S. Essakiappan, H. Krishnamoorthy, P. Enjeti, R. Balog, and S. Ahmed, "Independent control of series connected utility scale multilevel photovoltaic inverters," in *Proc. IEEE ECCE*, Raleigh, NC, USA, Sep. 2012, pp. 1760–1766.
- [12] G. Konstantinou, S. Pulikanti, M. Ciobotaru, V. Agelidis, and K. Muttaqi, "The seven-level flying capacitor based ANPC converter for grid integration of utility-scale PV systems," in *Proc. IEEE PEDG*, Aalborg, Denmark, Jun. 2012, pp. 592–597.
- [13] G. Brando, A. Danner, A. Del Pizzo, and R. Rizzo, "A high performance control technique of power electronic transformers in medium voltage grid-connected PV plants," in *Proc. ICEM*, Rome, Italy, Sep. 2010, vol. 2, pp. 1–6.
- [14] G. Buticchi, E. Lorenzani, and G. Franceschini, "A five-level single-phase grid-connected converter for renewable distributed systems," *IEEE Trans. Ind. Electron.*, vol. 60, no. 3, pp. 906–918, Mar. 2013.
- [15] Y. Kashihara and J. Itoh, "The performance of the multilevel converter topologies for PV inverter," in *Proc. CIPS*, Beijing, China, Mar. 2012, pp. 1–6.
- [16] Y. Noge and J. Itoh, "Multi-level inverter with H-bridge clamp circuit for single-phase three-wire grid connection suitable for super-junction–SiC MOSFET," in *Proc. IPERC*, Harbin, China, Jun. 2012, vol. 2, pp. 88–93.
- [17] C. Cecati, F. Ciancetta, and P. Siano, "A multilevel inverter for photovoltaic systems with fuzzy logic control," *IEEE Trans. Ind. Electron.*, vol. 57, no. 12, pp. 4115–4125, Dec. 2010.
- [18] I. Abdalla, J. Corda, and L. Zhang, "Multilevel dc-link inverter and control algorithm to overcome the PV partial shading," *IEEE Trans. Power Electron.*, vol. 28, no. 1, pp. 14–18, Jan. 2013.
- [19] J. Chavarria, D. Biel, F. Guinjoan, C. Meza, and J. Negroni, "Energybalance control of PV cascaded multilevel grid-connected inverters under level-shifted and phase-shifted PWMS," *IEEE Trans. Ind. Electron.*, vol. 60, no. 1, pp. 98–111, Jan. 2013.
- [20] A. Bidram, A. Davoudi, and R. Balog, "Control and circuit techniques to mitigate partial shading effects in photovoltaic arrays," *IEEE J. Photovoltaics*, vol. 2, no. 4, pp. 532–546, Oct. 2012.

- [21] G. Grandi, C. Rossi, D. Ostojic, and D. Casadei, "A new multilevel conversion structure for grid-connected PV applications," *IEEE Trans. Ind. Electron.*, vol. 56, no. 11, pp. 4416–4426, Nov. 2009.
- [22] S. Daher, J. Schmid, and F. Antunes, "Multilevel inverter topologies for stand-alone PV systems," *IEEE Trans. Ind. Electron.*, vol. 55, no. 7, pp. 2703–2712, Jul. 2008.
- [23] F. Filho, L. Tolbert, Y. Cao, and B. Ozpineci, "Real-time selective harmonic minimization for multilevel inverters connected to solar panels using artificial neural network angle generation," *IEEE Trans. Ind. Appl.*, vol. 47, no. 5, pp. 2117–2124, Sep./Oct. 2011.
- [24] P. Cortes, S. Kouro, F. Barrios, and J. Rodriguez, "Predictive control of a single-phase cascaded H-bridge photovoltaic energy conversion system," in *Proc. IPEMC*, Harbin, China, Jun. 2012, vol. 2, pp. 1423–1428.
- [25] N. Rahim, K. Chaniago, and J. Selvaraj, "Single-phase seven-level gridconnected inverter for photovoltaic system," *IEEE Trans. Ind. Electron.*, vol. 58, no. 6, pp. 2435–2443, Jun. 2011.
- [26] N. Rahim and J. Selvaraj, "Multistring five-level inverter with novel PWM control scheme for PV application," *IEEE Trans. Ind. Electron.*, vol. 57, no. 6, pp. 2111–2123, Jun. 2010.
- [27] J. Selvaraj and N. Rahim, "Multilevel inverter for grid-connected PV system employing digital PI controller," *IEEE Trans. Ind. Electron.*, vol. 56, no. 1, pp. 149–158, Jan. 2009.
- [28] J. Leon, R. Portillo, S. Vazquez, J. Padilla, L. Franquelo, and J. Carrasco, "Simple unified approach to develop a time-domain modulation strategy for single-phase multilevel converters," *IEEE Trans. Ind. Electron.*, vol. 55, no. 9, pp. 3239–3248, Sep. 2008.
- [29] Y.-H. Liao and C.-M. Lai, "Newly-constructed simplified single-phase multistring multilevel inverter topology for distributed energy resources," *IEEE Trans. Power Electron.*, vol. 26, no. 9, pp. 2386–2392, Sep. 2011.
- [30] M. Cavalcanti, A. Farias, K. Oliveira, F. Neves, and J. Afonso, "Eliminating leakage currents in neutral point clamped inverters for photovoltaic systems," *IEEE Trans. Ind. Electron.*, vol. 59, no. 1, pp. 435–443, Jan. 2012.
- [31] A. Varschavsky, J. Dixon, M. Rotella, and L. Moran, "Cascaded nine-level inverter for hybrid-series active power filter, using industrial controller," *IEEE Trans. Ind. Electron.*, vol. 57, no. 8, pp. 2761–2767, Aug. 2010.
- [32] V. Antunes, V. Pires, and J. Silva, "Narrow pulse elimination PWM for multilevel digital audio power amplifiers using two cascaded H-bridges as a nine-level converter," *IEEE Trans. Power Electron.*, vol. 22, no. 2, pp. 425–434, Mar. 2007.
- [33] D. Zambra, C. Rech, and J. Pinheiro, "Comparison of neutral-point clamped, symmetrical, and hybrid asymmetrical multilevel inverters," *IEEE Trans. Ind. Electron.*, vol. 57, no. 7, pp. 2297–2306, Jul. 2010.
- [34] S. Vazquez, J. Leon, L. Franquelo, J. Padilla, and J. Carrasco, "DC voltage- ratio control strategy for multilevel cascaded converters fed with a single DC source," *IEEE Trans. Ind. Electron.*, vol. 56, no. 7, pp. 2513–2521, Jul. 2009.
- [35] S. Vazquez, J. Leon, J. Carrasco, L. Franquelo, E. Galvan, M. Reyes, J. Sanchez, and E. Dominguez, "Analysis of the power balance in the cells of a multilevel cascaded H-bridge converter," *IEEE Trans. Ind. Electron.*, vol. 57, no. 7, pp. 2287–2296, Jul. 2010.

# International Conference on Advance Studies in Engineering and Sciences

Sri Satya Sai University of Technology and Medical Sciences , Sehore (M.P.)

ICASES-17

2<sup>nd</sup> December 2017, [www.conferenceworld.in](http://www.conferenceworld.in)

ISBN: 978-93-86171-83-2

[36] S. Lu, S. Mariethoz, and K. Corzine, "Asymmetrical cascade multilevel converters with noninteger or dynamically changing dc voltage ratios: Concepts and modulation techniques," *IEEE Trans. Ind. Electron.*, vol. 57, no. 7, pp. 2411–2418, Jul. 2010.

[37] B. Gu, J. Dominic, J.-S. Lai, C.-L. Chen, T. LaBella, and B. Chen, "High reliability and efficiency single-phase transformerless inverter for gridconnected photovoltaic systems," *IEEE Trans. Power Electron.*, vol. 28, no. 5, pp. 2235–2245, May 2013.